

ICOM

**SERVICE
MANUAL**

VHF TRANSCEIVER

IC-F14/F15/S

**Pre-release version
(Draft only)**

SECTION 1

SPECIFICATIONS

1. GENERAL

- Frequency coverage : 136.000–174.000 MHz
- Mode : FM
- Type of emission :

VERSION	WIDE	MIDDLE	NARROW
[USA], [GEN]	16K0F3E (25.0kHz)	N/A	11K0F3E (12.5 kHz)
[EUR]		14K0F3E (20.0 kHz)	8K0F3E (12.5 kHz)

- Number of conventional channels : 2 ch (IC-F14S/F15S), 16 ch (IC-F14/F15)
- Antenna impedance : 50 Ω
- Operating temperature range : –30°C to +60°C (–22 °F to +140°F) [USA], [GEN]
–25°C to +55°C [EUR]
- Power supply requirement : 7.2 V DC nominal (negative ground)
- Current drain (at 7.2 V DC ; approx.) :

RECEIVING		TRANSMITTING	
Stand-by	Max. audio	High (at 5 W)	Low (at 1 W)
70 mA	300 mA	1.5 A	0.7 A

- Dimensions (projections not included) : 53(W)×120(H)×38(D) mm
2³/₃₂(W)×4²³/₃₂(H)×1¹/₂(D) in
- Weight (Including BP-231) : Approximately 260 g (9³/₁₆ oz)

2. TRANSMITTER

- Output power (at 7.2 V DC) : 5 W
- Modulation : Variable reactance frequency modulation
- Maximum permissible deviation : ±5.0 kHz (Wide), ±4.0 kHz (Middle), ±2.5 kHz (Narrow)
- Frequency error : ±2.5 ppm
- Spurious emissions : 80 dB (typical) [USA], [GEN]
0.25 μW (≤ 1 GHz), 1.0 μW (> 1 GHz) [EUR]
- Adjacent channel power : 70 dB min. (Wide, Middle ; 80dB typ.)
60 dB min. (Narrow ; 70dB typ.)
- Audio harmonic distortion : 3% typical (AF 1 kHz, 40% deviation)
- Hum and Noise ([USA], [GEN]) (without CCITT filter) : 40 dB min. (46 dB typical) for Wide
34 dB min. (40 dB typical) for Narrow
- Residual modulation ([EUR] only) (with CCITT filter) : 45 dB min. (55 dB typical) for Wide
43 dB min. (53 dB typical) for Middle
40 dB min. (50 dB typical) for Narrow
- Limiting charact of modulator : 60–100% of maximum deviation
- Microphone impedance : 2.2 kΩ

3. RECEIVER

- Receive system : Double conversion superheterodyne system
- Intermediate frequencies : 1st IF: 46.35 MHz, 2nd IF: 450 kHz
- Sensitivity : 0.25 μV (–119 dBm) typical at 12 dB SINAD [USA], [GEN]
–4 dBμ (–111 dBm) emf typical at 20 dB SINAD [EUR]
- Adjacent channel selectivity : 70 dB min. (75 dB typical) for Wide and Middle
60 dB min. (65 dB typical) for Narrow
- Spurious response : 70 dB min.
- Intermodulation rejection ratio : 70 dB min. (74 dB typical) [USA], [GEN]
65 dB min. (67 dB typical) [EUR]
- Hum and Noise ([USA], [GEN] only) (without CCITT filter) : 40 dB min. (50 dB typical) for Wide
34 dB min. (45 dB typical) for Narrow
- Hum and Noise ([EUR] only) (with CCITT filter) : 45 dB min. (55 dB typical) for Wide
43 dB min. (53 dB typical) for Middle
40 dB min. (50 dB typical) for Narrow
- Audio output power : 0.5 W typical at 5% distortion with an 8 Ω load
- Squelch sensitivity (at threshold) : 0.25 μV typical [USA], [GEN]
–4 dBμV (EMF) typical [EUR]
- Output impedance (Audio) : 8 Ω

Specifications are measured in accordance with EIA-152-C/204D, TIA-603 or EN 300 086.

All stated specifications are subject to change without notice or obligation.

SECTION 2

CIRCUIT DESCRIPTION

2-1 RECEIVER CIRCUITS

2-1-1 ANTENNA SWITCHING CIRCUIT

The antenna switching circuit functions as a low-pass filter while receiving and a resonator circuit while transmitting. This circuit does not allow transmit signals to enter the receiver circuits.

Received signals enter the antenna connector (CHASSIS; J1) and pass through the low-pass filter (L1–L3, C2–C5, C175, C176). The filtered signals are passed through the $1/4\lambda$ type antenna switching circuit (D5, D6, L6, L7) and then applied to the RF circuit.

2-1-2 RF CIRCUIT

The RF circuit amplifies signals within the range of frequency coverage and filters out-of-band signals.

The signals from the antenna switching circuit pass through the bandpass filter (D4, D8, L8, L9). The filtered signals are amplified at the RF amplifier (Q2) and then passed through the another bandpass filter (D9, D10, L11) to suppress unwanted signals. The filtered signals are applied to the 1st mixer circuit.

D4, D8–D10 employ varactor diodes, that are controlled by the CPU via the D/A converter (IC8), to track the bandpass filter. These varactor diodes tune the center frequency of an RF passband for wide bandwidth receiving and good image response rejection.

2-1-3 1ST MIXER AND 1ST IF CIRCUITS

The 1st mixer circuit converts the received signal into fixed frequency of the 1st IF signal with the PLL output frequency. By changing the PLL frequency, only the desired frequency passes through a crystal filter at the next stage of the 1st mixer.

The RF signals from the bandpass filter are mixed with the 1st LO signals, where come from the RX VCO circuit via the attenuator (R26–R28), at the 1st mixer circuit (Q3) to produce a 46.35 MHz 1st IF signal. The 1st IF signal is passed through a monolithic filter (F1) in order to obtain selection capability and to pass only the desired signal. The filtered signal is applied to the 2nd IF circuit after being amplified at the 1st IF amplifier (Q4).

2-1-4 2ND IF AND DEMODULATOR CIRCUITS

The 2nd mixer circuit converts the 1st IF signal into a 2nd IF signal. The double-conversion superheterodyne system (which convert receive signals twice) improves the image rejection ratio and obtains stable receiver gain.

The 1st IF signal from the IF amplifier (Q4) is applied to the 2nd mixer section of the FM IF IC (IC1, pin 16), and is mixed with the 2nd LO signal to be converted into a 450 kHz 2nd IF signal.

The FM IF IC (IC1) contains the 2nd mixer, 2nd local oscil-

lator, limiter amplifier, quadrature detector, active filter and noise amplifier circuits. A 2nd LO signal (45.9 MHz) is produced at the PLL circuit by tripling it's reference frequency (15.3 MHz).

The 2nd IF signal from the 2nd mixer (IC1, pin 3) passes through the ceramic filter (F12) to remove unwanted heterodyned frequencies. It is then amplified at the limiter amplifier section (IC1, pin 5) and applied to the quadrature detector section (IC1, pins 10, 11) to demodulate the 2nd IF signal into AF signals.

The demodulated AF signals are output from pin 9 (IC1) as "DET" signal, and are then applied to the AF circuit.

2-1-5 AF AMPLIFIER CIRCUIT

The AF amplifier circuit amplifies the demodulated AF signals to drive a speaker.

The AF signals from the FM IF IC (IC1, pin 9) pass through the high-pass filter (IC6, pins 3 and 1) to suppress unwanted harmonic components. The signals pass through the RX mute switch (Q34) which is controlled by "RMUT" signal from the CPU (IC13, pin 56), and are then applied to another high-pass filter (IC6, pins 13 and 14). The filtered signals pass through the low-pass filter (IC6, pins 6 and 7) via the analog switch (IC10, pins 1 and 2). The signals are applied to the analog switch (IC10, pin 10) again, and are then applied to the AF power amplifier (IC12, pin 4) via the AF volume (R226). The amplified AF signals are output from pin 10, and are then applied to the speaker which is connected with J1 via the J3.

2-1-6 RECEIVE MUTE CIRCUITS

• NOISE SQUELCH

A squelch circuit cuts out AF signals when no RF signals are received. By detecting noise components in the AF signals, the squelch circuit switches the AF mute switch.

Some noise components in the AF signals from the FM IF IC (IC1, pin 9) are applied to the D/A converter (IC8, pin 1) as "DET" signal, and are then output from pin 2. The signals are applied to the active filter section in the FM IF IC (IC1, pin 8). The active filter section filters and amplifies noise components. The amplified signals are converted into the pulse-type signals at the noise detector section and output from pin 13 as "NOIS" signal.

The "NOIS" signal from the FM IF IC is applied to the CPU (IC13, pin 53). Then the CPU analyzes the noise condition and outputs the AF mute control signal from the CPU (pin 56) as "RMUT" signal. The signal is applied to the RX mute controller (Q34) to control the AF signal muting.

• CTCSS AND DTCS

The tone squelch circuit detects tone signals and opens the squelch only when receiving a signal containing a matched subaudible tone (CTCSS or DTCS). When tone squelch is in use, and a signal with a mismatched or no subaudible tone is received, the tone squelch circuit mutes the AF signals even when noise squelch is open.

A portion of the “DET” signals from the FM IF IC (IC1, pin 9) passes through the low-pass filter (IC7, pins 10 and 8) to remove AF (voice) signals, and are then applied to the amplifier (IC7, pin 12). The amplified signals are applied to the CTCSS or DTCS decoder inside of the CPU (IC13, pin 60) as the “CDEC” signal. The CPU outputs AF mute control signal, and is then applied to the AF mute controller (Q34) and analog switch (IC10, pins 12 and 13) to control AF signals muting as “RMUT” signal.

2-2 TRANSMITTER CIRCUITS

2-2-1 MICROPHONE AMPLIFIER CIRCUIT

The microphone amplifier circuit amplifies audio signals within +6 dB/octave pre-emphasis characteristics from the microphone to a level needed for the modulation circuit.

The AF signals from the microphone are passed through the microphone mute switch (Q35), and are then applied to the amplifier (IC6, pins 9 and 8) via the high-pass filter (IC6, pins 13 and 14). The amplified signals are applied to the analog switch (IC10, pin 4), and outputs from pin 3. The signals pass through the low-pass filter (IC6, pins 6 and 7), then applied to the analog switch (IC10, pin 9) again. The signals are applied to the D/A converter (IC8, pin 4). The converted signals output from pin 3, and applied to the modulation circuit (D18) as “MOD” signal.

2-2-2 MODULATION CIRCUIT

The modulation circuit modulates the VCO oscillating signal (RF signal) using the microphone audio signals.

The AF signals from the D/A converter (IC8, pin 3) change the reactance of varactor diode (D18) to modulate the oscillated signal at the TX VCO circuit (Q13, D16, D17). The modulated VCO signal is amplified at the buffer amplifiers (Q12, Q10) and is then applied to the drive amplifier circuit via the T/R switch (D14).

The CTCSS/DTCS signals (“CENC0”, “CENC1”, “CENC2”) from the CPU (IC13, pins 23–25) pass through the low-pass filter (IC5, pins 12 and 14) via 3 registers (R191, R192, R193) to change its wave form. Then the signals are applied to the D/A converter (IC8, pin 9). The output signals from the D/A converter (IC8, pin 10) pass through the low-pass filter (IC6, pins 6 and 7) to be mixed with “MOD” signal at the filter (IC6), and are then applied to the D/A converter again (IC8, pin 4).

2-2-3 DRIVE/POWER AMPLIFIER CIRCUITS

The drive/power amplifier circuits amplify the VCO oscillating signal to an output power level.

The modulated RF signal from the TX VCO circuit passes through the T/R switch (D14), and is amplified at the pre-drive (Q9), drive (Q8) and power (Q7) amplifiers to obtain 5 W of RF power (at 7.2 V DC).

The amplified signal passes through the power detector (D1), antenna switching circuit (D2) and low-pass filter (L1–L3, C2–C5, C175, C176), and is then applied to the antenna connector (CHASSIS unit; J1).

The bias current of the pre-drive (Q9), drive (Q8) and power (Q7) amplifiers are controlled by the APC circuit.

2-2-4 APC CIRCUIT

The APC circuit (IC2, D1) protects drive and power amplifiers from the reflected wave, and selects output power of HIGH, LOW2 or LOW1.

The power detector (D1) detects a portion of transmit power output and converts it into DC voltage. The DC voltage is at a minimum level when the antenna impedance is matched to 50 Ω , and increased when mismatched.

The detected voltage is applied to the differential amplifier (IC2, pin 3), and the “T2” signal from the D/A converter (IC8, pin 23), controlled by the CPU (IC13), is applied to the other input for reference. When antenna impedance is mismatched, the detected voltage exceeds the power setting voltage. Then the output voltage of the differential amplifier (IC2, pin 4) controls the input current of the pre-drive (Q9), drive (Q8) and power (Q7) amplifiers to reduce the output power.

2-3 PLL CIRCUITS

2-3-1 PLL CIRCUIT

A PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL output compares the phase of the divided VCO frequency to the reference frequency. The PLL output frequency is controlled by the divided ratio (N-data) of a programmable divider.

The PLL circuit contains the TX/RX VCO circuits (TX: Q13, D16, D17; RX: Q14, D19, D20). The oscillated signal is amplified at the buffer amplifiers (Q11, Q12) and then applied to the PLL IC (IC4, pin 8) after being passed through the low-pass filter (L32, C206–C208).

The filtered signal outputs from pins 15 and 16, and is then applied to the charge pump (Q39, Q40). The signal passes through the loop filter (C146, C147, C149, R95–R97), and is then applied to the TX and RX VCO circuits as a lock voltage.

The PLL IC contains a prescaler, programmable counter, programmable divider and phase detector, etc. The entered signal is divided at the prescaler and programmable counter section by the N-data ratio from the CPU. The divided signal is detected on phase at the phase detector using the reference frequency.

If the oscillated signal drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the oscillated frequency.

2-3-2 VCO CIRCUIT

The VCO circuit contains a separate RX VCO (Q14, D19, D20) and TX VCO (Q13, D16, D17). The oscillated signal is amplified at the buffer amplifiers (Q10, Q12) and is then applied to the T/R switch (D14 for TX, D15 for RX). Then the receive 1st LO (RX) signal is applied to the 1st mixer circuit (Q3) and the transmit (TX) signal to the pre-drive amplifier (Q9).

A portion of the signal from the buffer amplifier (Q12) is fed back to the PLL IC (IC4, pin 8) via the buffer amplifier (Q11) and low-pass filter (L32, C206–C208) as the comparison signal.

2-4 OTHER CIRCUITS

LED CONTROL CIRCUIT

The LED control circuit is composed of the CPU (IC13), LED driver (Q32) and LED (DS1).

The CPU outputs “RLED” and “TLED” signals from the pins 42 and 43. The signals are applied to the LED driver (Q32, pins 3 and 1). The driver outputs LED control signals to the LED (DS1).

CONDITION	COLOR
RECEIVING (2/5-TONE CODE)	ORANGE (Lighting)
LOW BATTERY (Nearly exhausted)	RED (Blinks Slowly)
LOW BATTERY (Almost exhausted)	RED (Blinks Fast)
CLONING	ORANGE (Blinking)
RECEIVING/SQUELCH OPEN	GREEN (Lighting)
TRANSMITTING	RED (Lighting)

2-5 POWER SUPPLY CIRCUIT

VOLTAGE LINE

LINE	DESCRIPTION
VCC	The voltage from the connected battery pack.
+5V	Common 5 V converted from the VCC line at the +5 regulator circuit (IC9). The output voltage is supplied to the D/A converter (IC8), analog SW (IC10) and so on.
S5V	Common 5 V converted from the VCC line at the S5 regulator circuit (Q23–Q25). The output voltage is supplied to the ripple filter (Q17), PLL IC (IC4), etc.
R5V	Receive 5 V converted from the S5V line at the R5 regulator circuit (Q22). The output voltage is supplied to the tripler (Q19), FM IF IC (IC1), IF amplifier (Q4), VCO switch (Q15, Q16), 1st mixer (Q3), etc.
T5V	Transmit 5 V converted from the S5V line at the T5 regulator circuit (Q21). The output voltage is supplied to the pre-drive (Q9), APC amplifier (IC2).

2-6 PORT ALLOCATION

D/A CONVERTER IC (IC8)

Pin number	Port name	Description
11	BAL	Outputs the modulation balance level control signal. The signal is applied to the buffer amplifier (IC7, pin 3).
23	T2	<ul style="list-style-type: none"> Outputs the bandpass filter tuning signal during receive. The output signal is applied to the bandpass filters (D9, D10). Outputs the TX power control signal during transmit. The output signal is applied to the APC amplifier (IC2, pin 1).
22	T1	Outputs the bandpass filter tuning signal. The output signal is applied to the bandpass filters (D4, D8).
14	LVA	Outputs the PLL lock voltage control signal. The output signal is applied to the buffer amplifier (IC7, pin 3).
15	REF	Outputs the reference oscillator correcting voltage. The voltage is applied to the buffer amplifier (IC7, pin 5).

SECTION 3 ADJUSTMENT PROCEDURES

3-1 PREPARATION

When adjusting IC-F14/S, the optional CS-F14 ADJ ADJUSTMENT SOFTWARE (Rev. 1.0 or later), JIG cable and OPC-478 CLONING CABLE (RS-232 type) or OPC-478U (USB type) are required.

- REQUIRED TEST EQUIPMENT -

EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output voltage : 7.2 V DC Current capacity : 5 A or more	Audio generator	Frequency range : 300–3000 Hz Output level : 1–500 mV
FM deviation meter	Frequency range : DC–300 MHz Measuring range : 0 to ±10 kHz	Attenuator	Power attenuation : 40 or 50 dB Capacity : 10 W or more
Frequency counter	Frequency range : 0.1–300 MHz Frequency accuracy : ±1 ppm or better Sensitivity : 100 mV or better	Standard signal generator (SSG)	Frequency range : 100–300 MHz Output level : 0.1 μV–32 mV (–127 to –17 dBm) (As open circuit.)
Digital multimeter	Input impedance : 10 MΩ/V DC or better	DC voltmeter	Input impedance : 50 kΩ/V DC or better
RF power meter (terminated type)	Measuring range : 1–20 W Frequency range : 100–300 MHz Impedance : 50 Ω SWR : Less than 1.2 : 1	Oscilloscope	Frequency range : DC–20 MHz Measuring range : 0.01–20 V
		AC millivoltmeter	Measuring range : 10 mV–10 V

- SYSTEM REQUIREMENTS -

- Microsoft® Windows® 98/SE/ME/2000/XP
- RS232C/USB port

- BEFORE STARTING SOFTWARE ADJUSTMENT -

Clone adjustment frequencies, TX power, CTCSS frequency, DTCS code and IF bandwidth (see ADJUSTMENT CONFIGURATION on the next page) into the transceiver using with the CS-F14 CLONING SOFTWARE before starting SOFTWARE ADJUSTMENTS. Otherwise, the transceiver can not be adjusted.

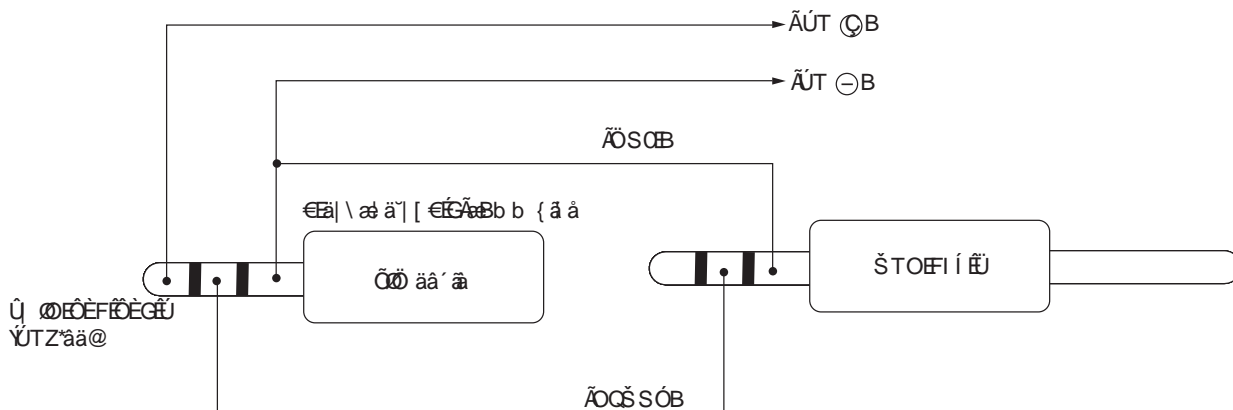
CAUTION!: BACK UP the originally programmed memory data in the transceiver before programming the adjustment frequencies. When program the adjustment frequencies into the transceiver, the transceiver's memory data will be overwritten and lose original memory data at the same time.

- STARTING SOFTWARE ADJUSTMENT -

- (1) Connect IC-F14 and PC with OPC-478/U and JIG CABLE.
- (2) Turn the transceiver power ON.
- (3) Boot up Windows, and click the program group 'CS-F14 ADJ' in the 'Programs' folder of the [Start] menu, then CS-F14 ADJ's window appears.
- (4) Click 'Connect' on the CS-F14's window, then appears IC-F14's up-to-date condition.
- (5) Set or modify adjustment data as desired.

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- JIG CABLE -



3-1-2 CONFIGURATION LIST FOR ADJUSTMENTS

- IC-F14/F15 -

CHANNEL	FREQUENCY	SETTING CONDITION	CHANNEL	FREQUENCY	SETTING CONDITION
1	174.000 MHz	Set TX power to High.	6	155.000 MHz	Set TX power to Low1. Set IF bandwidth to Narrow.
2	174.000 MHz	Set TX power to Low2.	7	155.000 MHz	Set TX power to Low1. Set IF bandwidth to Wide/Middle. (Middle : EUR only)
3	174.000 MHz	Set TX power to Low1.			
4	174.000 MHz	Set TX power to Low1. Set DTCS code to 007. Set IF bandwidth to Narrow.	8	155.000MHz	Set TX power to Low1. Set IF bandwidth to Wide. Set CTCSS to151.400Hz.
5	174.000 MHz	Set TX power to Low1. Set DTCS code to 007. Set IF bandwidth to Wide/Middle. (Middle : EUR only)	9	136.000 MHz	Set IF bandwidth to Wide.

- IC-F14S/F15S -

- Clone adjustment frequency and setting condition everytime adjusting each item (1-8).

1.PLL LOCK VOLTAGE

CH.	FREQUENCY	SETTING CONDITION
1	174.000 MHz	Set TX power to Low1
2	136.000 MHz	Set IF bandwidth to Wide.

2.REFERENCE FREQUENCY

CH.	FREQUENCY	SETTING CONDITION
1	174.000 MHz	Set TX power to Low1

3.TX POWER

CH.	FREQUENCY	SETTING CONDITION
1	174.000 MHz	Set TX power to High.
2	174.000 MHz	Set TX power to Low2.
3	174.000 MHz	Set TX power to Low1.

4.FM DEVIATION

CH.	FREQUENCY	SETTING CONDITION
1	155.000 MHz	Set TX power to Low1. Set IF bandwidth to Narrow..
2	155.000 MHz	Set TX power to Low1. Set IF bandwidth to Wide/Middle. (Middle : EUR only)

5.MODULATION BALANCE

CH.	FREQUENCY	SETTING CONDITION
1	174.000 MHz	Set TX power to Low1. Set DTCS code to 007. Set IF bandwidth to Narrow..
2	174.000 MHz	Set TX power to Low1. Set DTCS code to 007. Set IF bandwidth to Wide/Middle. (Middle : EUR only)

6.CTCSS/DTCS DEVIATION

CH.	FREQUENCY	SETTING CONDITION
1	155.000MHz	Set TX power to Low1. Set IF bandwidth to Wide. Set CTCSS to151.400Hz..

7.RX SENSITIVITY

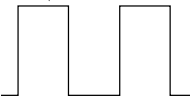
CH.	FREQUENCY	SETTING CONDITION
1	136.000 MHz	Set IF bandwidth to Wide.

8.SQUELCH LEVEL

CH.	FREQUENCY	SETTING CONDITION
1	155.000MHz	Set TX power to Low1. Set IF bandwidth to Wide..

3-2 SOFTWARE ADJUSTMENTS (TRANSMITTING)

Select an operation using [↑] / [↓] keys, then set specified value using [←] / [→] keys on the connected computer keyboard.

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE	
		UNIT	LOCATION		
PLL LOCK VOLTAGE [LV (RX LVA)] [LV (TX LVA)]	1 • Operating CH : CH3 (*CH1) • Receiving	PC screen	Check the "LV" item on the CS-F14 ADJ's screen.	3.5 V	
	2 • Operating CH : CH3 (*CH1) • Connect an RF power meter or 50 Ω dummy load to the antenna connector. • Transmitting			3.5 V	
	3 • Operating CH : CH9 (*CH2) • Receiving	MAIN	Connect the digital multimeter to the "LV" line.	0.9–1.5 V (Verify)	
	4 • Operating CH : CH9 (*CH2) • Transmitting			0.9–1.5 V (Verify)	
REFERENCE FREQUENCY [REF]	1 • Operating CH : CH3 (*CH1) • Transmitting	Top panel	Loosely couple the frequency counter to the antenna connector.	174.0000 MHz	
OUTPUT POWER [Power (Hi)] [Power (L2)] [Power (L1)]	1 • Operating CH : CH1 (*CH1) • Transmitting	Top panel	Connect the RF power meter to the antenna connector.	5.0 W	
	2 • Operating CH : CH2 (*CH2) • Transmitting			2.0 W	
	3 • Operating CH : CH3 (*CH3) • Transmitting			1.0 W	
FM DEVIATION [MOD N] (Narrow)	1 • Operating CH : CH6 (*CH1) • Set the deviation meter as: HPF :OFF LPF :20kHz De-emphasis :OFF Detector :(P_P)/2 • Connect the audio generator the [MIC] Connector and set as :1.0kHz • Transmitting	Top panel	Connect the FM deviation meter to the antenna connector through the attenuator.	±2.10 kHz	
	[MOD Ratio] (Middle; EUR only)			2 • Operating CH : CH7 (*CH2) • Transmitting	±3.20 kHz
	[MOD Ratio] (Wide)			3 • Operating CH : CH7 (*CH2) • Transmitting	±4.10 kHz
MODULATION BALANCE [BAL N] (Narrow)	1 • Operating CH : CH4 (*CH1) • No audio applied to the [MIC] input. • Set the deviation meter as: HPF :OFF LPF :20kHz De-emphasis :OFF Detector :(P_P)/2 • Transmitting	Top panel	Connect the FM deviation meter with the oscilloscope to the antenna connector through an attenuator.	<p>Set to square wave form</p> 	
	[BAL Ratio] (Middle; EUR only)				2 • Operating CH : CH5 (*CH2) • Transmitting
	[BAL Ratio] (Wide)				3 • Operating CH : CH5 (*CH2) • Transmitting
CTCSS/DTCS DEVIATION [CTCS/DTCS]	1 • Operating CH : CH8 (*CH1) • No audio applied to the [MIC] input. • Transmitting	Top panel	Connect the FM deviation meter to the antenna connector through the attenuator.	±0.68 kHz	

*For F14S/F15S adjustment.

SOFTWARE ADJUSTMENTS (RECEIVING)

- Select an operation using [↑] / [↓] keys, then set specified value using [←] / [→] keys on the connected computer keyboard.

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE	
		UNIT	LOCATION		
RX SENSITIVITY [BPF T1], [BPF T2]	1	<ul style="list-style-type: none"> • Operating CH : CH11 • Connect a standard signal generator to the antenna connector and set as: Frequency : 136.000 MHz Level : 10 μV (-87 dBm) Modulation : 1 kHz Deviation : \pm3.5 kHz • Receiving 	Side pannel	Connect a SINAD meter with an 8 Ω load to the [SP] jack through the JIG cable.	Minimum distortion level
	<p>CONVENIENT: The BPF T1, BPF T2 can be adjusted automatically. 1-1: Set the cursor to "BPF ALL" on the adjustment program and then push [ENTER] key. 1-2: The connected PC tunes BPF T1, BPF T2 to peak levels. or 2-1: Set the cursor to one of BPF T1, T2 as desired. 2-2: Push [ENTER] key to start tuning. 2-3: Repeat 2-1 and 2-2 to perform additional BPF tuning.</p>				
SQUELCH LEVEL [SQL]	1	<ul style="list-style-type: none"> • Operating CH : CH9 (*1CH) • Connect an SSG to the antenna connector and set as: Frequency : 155.000MHz Level : 0.18 μV (-122 dBm) Modulation : 1 kHz Deviation : \pm3.5 kHz • Receiving 	Side panel	Connect a speaker to the [SP] jack through the JIG cable	Set "SQL level" to close squelch. Then set "SQL level" at the point where the audio signals just appears.

*For F14S/F15S adjustment.

SECTION 4

PARTS LIST

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
IC1	1110003200	S.IC TA31136FN (EL)		
IC2	1110002750	S.IC TA75S01F (TE85R)		
IC4	1140005990	S.IC MB15A02PFV1-G-BND-ER		
IC5	1110005340	S.IC NJM12902V-TE1		
IC6	1110005320	S.IC NJM13403V-TE1		
IC7	1110005340	S.IC NJM12902V-TE1		
IC8	1190000350	S.IC M62363FP-650C		
IC9	1110005350	S.IC NJM2870F05-TE1		
IC10	1130011770	S.IC CD4066BPWR		
IC12	1110001810	S.IC TA7368F (ER)		
IC13	1140010770	S.IC HD64F3687FP (EMPTY)		
IC14	1110006260	S.IC BD5242G-TR		
IC15	1130011540	S.IC BR24L16FV-WE2		
Q1	1560000840	S.FET 2SK1829 (TE85R)		
Q2	1580000730	S.FET 3SK293 (TE85L)		
Q3	1580000760	S.FET 3SK299-T1 U73		
Q4	1530002600	S.TR 2SC4215-O (TE85R)		
Q6	1590003230	S.TR UNR9113J-(TX)		
Q7	1560001230	S.FET RD07MVS1		
Q8	1560001240	S.FET RD01MUS1		
Q9	1530003420	S.TR 2SC5110-O (TE85R)		
Q10	1530003310	S.TR 2SC5107-O (TE85R)		
Q11	1530003310	S.TR 2SC5107-O (TE85R)		
Q12	1530003310	S.TR 2SC5107-O (TE85R)		
Q13	1530002920	S.TR 2SC4226-T1 R25		
Q14	1530002920	S.TR 2SC4226-T1 R25		
Q15	1590001400	S.TR XP1214 (TX)		
Q16	1590003290	S.TR UNR9213J-(TX)		
Q17	1530002850	S.TR 2SC4116-BL (TE85R)		
Q18	1560000540	S.FET 2SK880-Y (TE85R)		
Q19	1530002850	S.TR 2SC4116-BL (TE85R)		
Q20	1560001360	S.FET 2SK3019 TL		
Q21	1510000920	S.TR 2SA1577 T106 Q		
Q22	1510000920	S.TR 2SA1577 T106 Q		
Q23	1520000460	S.TR 2SB1132 T100 R		
Q24	1590001190	S.TR XP6501-(TX) .AB		
Q25	1590003230	S.TR UNR9113J-(TX)		
Q26	1590003290	S.TR UNR9213J-(TX)		
Q27	1590003290	S.TR UNR9213J-(TX)		
Q28	1590003430	S.TR UNR911HJ-(TX)		
Q29	1590003270	S.TR UNR9210J-(TX)		
Q30	1510001080	S.TR 2SA2048 TLR		
Q31	1590001190	S.TR XP6501-(TX) .AB		
Q32	1590003210	S.TR XP1111 (TX)		
Q33	1590003230	S.TR UNR9113J-(TX)		
Q34	1560001360	S.FET 2SK3019 TL		
Q35	1560001360	S.FET 2SK3019 TL		
Q39	1530002060	S.TR 2SC4081 T106 R		
Q40	1510000510	S.TR 2SA1576A T106R		
D1	1790001790	S.DIO RB876W TL		
D2	1750000580	S.DIO 1SV307 (TPH3)		
D4	1750000720	S.VCP HVC375BTRF		
D5	1750000580	S.DIO 1SV307 (TPH3)		
D6	1790001260	S.DIO MA2S077-(TX)		
D8	1750000720	S.VCP HVC375BTRF		
D9	1750000710	S.VCP HVC350BTRF		
D10	1750000710	S.VCP HVC350BTRF		
D14	1790001260	S.DIO MA2S077-(TX)		
D15	1790001260	S.DIO MA2S077-(TX)		
D16	1750000770	S.VCP HVC376BTRF		
D17	1750000770	S.VCP HVC376BTRF		
D18	1720000470	S.VCP 1SV239 (TPH3)		
D19	1750000770	S.VCP HVC376BTRF		
D20	1750000770	S.VCP HVC376BTRF		
D21	1750000720	S.VCP HVC375BTRF		
D22	1750000720	S.VCP HVC375BTRF		
D23	1790001250	S.DIO MA2S111-(TX)		
D24	1790001250	S.DIO MA2S111-(TX)		
D26	1790001790	S.DIO RB876W TL		
D27	1750000520	S.DIO DAN222TL		
D28	1790001260	S.DIO MA2S077-(TX)		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
D29	1750000940	S.DIO ISS400 TE61		
F11	2030000150	S.MLH FL-335 (46.350 MHz)		
F12	2020001840	CER ALFYM450F=K		
X1	6070000190	S.DCR CDBC450KCAY24-R0 (CDBC450CX24)		
X2	6050011940	S.XTL CR-783 (15.3 MHz)		
X3	6050011720	S.XTL CR-764 (19.6608 MHz)		
L1	6200008580	S.COL 0.30-1.4-6TL 32N		
L2	6200008280	S.COL 0.30-1.7-7TL 50N		
L3	6200008170	S.COL 0.35-1.6-8TL 54N		
L4	6200008280	S.COL 0.30-1.7-7TL 50N		
L5	6200008490	S.COL 0.30-0.9-3TR 7.5N		
L6	6200008280	S.COL 0.30-1.7-7TL 50N		
L7	6200007870	S.COL ELJRF 39NJF2 (39)		
L8	6200008090	S.COL LQW2BHN68NJ01L (LQN21A 68NJ04)		
L9	6200008090	S.COL LQW2BHN68NJ01L (LQN21A 68NJ04)		
L10	6200007750	S.COL LQW2BHN56NJ01L (LQN21A 56NJ04)		
L11	6200007750	S.COL LQW2BHN56NJ01L (LQN21A 56NJ04)		
L12	6200009350	S.COL ELJRE R22G-F3		
L13	6200007850	S.COL ELJNC R82K-F		
L15	6200002860	S.COL NL 252018T-4R7J		
L16	6200009710	S.COL 0.30-0.9-4TL 10.5N		
L17	6200008210	S.COL 0.45-1.5-5TL 23.2N		
L18	6200005700	S.COL ELJRE 22NG-F		
L19	6200010400	S.COL ELJRE 39NJ-F		
L20	6200003590	S.COL EXCCL3225U1		
L21	6200011030	S.COL ELJRF R10JF2 (0.1)		
L22	6200011030	S.COL ELJRF R10JF2 (0.1)		
L23	6200011030	S.COL ELJRF R10JF2 (0.1)		
L24	6200003640	S.COL MLF1608E 100K-T		
L25	6200007760	S.COL LQW2BHN68NJ01L (LQN21A 68NJ04)		
L26	6200008090	S.COL LQW2BHN68NJ01L (LQN21A 68NJ04)		
L27	6200007170	S.COL MLF1608A 3R3K-T		
L28	6200007170	S.COL MLF1608A 3R3K-T		
L31	6200011020	S.COL ELJRF 82NJF (82)		
L32	6200007880	S.COL ELJRF 33NJF2 (33)		
L33	6200004480	S.COL MLF1608D R82K-T		
L35	6200003540	S.COL MLF1608D R22K-T		
L37	6200003640	S.COL MLF1608E 100K-T		
L38	6200008090	S.COL LQW2BHN68NJ01L (LQN21A 68NJ04)		
L39	6200007720	S.COL LQW2BHN33NJ01L (LQN21A 33NJ04)		
L42	6200007170	S.COL MLF1608A 3R3K-T		
L43	6200007170	S.COL MLF1608A 3R3K-T		
R1	7030003540	S.RES ERJ3GEYJ 682 V (6.8 kΩ)		
R2	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R3	7030005530	S.RES ERJ2GEJ 100 X (10Ω)		
R4	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R5	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R6	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R7	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R8	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		
R9	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R10	7030005030	S.RES ERJ2GEJ 152 X (1.5 kΩ)		
R11	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R12	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R13	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R14	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R15	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R16	7030008280	S.RES ERJ2GEJ 271 X (270 Ω)		
R17	7030004970	S.RES ERJ2GEJ 470 X (47 Ω)		
R18	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R19	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R20	7030009270	S.RES ERJ2GEJ 821 X (820 Ω)		
R21	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R22	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R23	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R24	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R25	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		

US1=F14 for USA, EU1=F14 for EURO, GE1=F14 for GEN S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R26	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R27	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R28	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R29	7030009160	S.RES ERJ2GEJ 181 X (180 Ω)		
R31	7030004970	S.RES ERJ2GEJ 470 X (47 Ω)		
R32	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R33	7030007280	S.RES ERJ2GEJ 331 X (330 Ω)		
R34	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R35	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R36	7030007300	S.RES ERJ2GEJ 332 X (3.3 kΩ)		
R37	7030007290	S.RES ERJ2GEJ 222 X (2.2 kΩ)		
R38	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R39	7030004970	S.RES ERJ2GEJ 470 X (47 Ω)		
R40	7030005030	S.RES ERJ2GEJ 152 X (1.5 kΩ)		
R42	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R43	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R44	7030006610	S.RES ERJ2GEJ 394 X (390 kΩ)		
R45	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R46	7030007290	S.RES ERJ2GEJ 222 X (2.2 kΩ)		
R48	7030005010	S.RES ERJ2GEJ 681 X (680 Ω)		
R50	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R51	7030003670	S.RES ERJ3GEJ 823 V (82 kΩ)		
R52	7030003350	S.RES ERJ3GEJ 181 V (180 Ω)		
R53	7030007250	S.RES ERJ2GEJ 220 X (22 Ω)		
R54	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R55	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R57	7030005590	S.RES ERJ2GEJ 680 X (68 Ω)		
R58	7030005060	S.RES ERJ2GEJ 333 X (33 kΩ)		
R59	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R61	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R62	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R63	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R65	7030007290	S.RES ERJ2GEJ 222 X (2.2 kΩ)		
R67	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R68	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R69	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R70	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R71	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R72	7030009320	S.RES ERJ2GEJ 4R7 X (4.7 Ω)		
R75	7030005700	S.RES ERJ2GEJ 274 X (270 kΩ)		
R76	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R77	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R78	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R79	7030008340	S.RES RR0510P-182-D (1.8 kΩ)		
R80	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R81	7030010040	S.RES ERJ2GE-JPW		
R82	7030009320	S.RES ERJ2GEJ 4R7 X (4.7 Ω)		
R83	7030008340	S.RES RR0510P-182-D (1.8 kΩ)		
R84	7030011000	S.RES RR0510P-392-D (3.9 kΩ)		
R85	7030011000	S.RES RR0510P-392-D (3.9 kΩ)		
R86	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R87	7030008370	S.RES ERJ2GEJ 561 X (560 Ω)		
R88	7030008370	S.RES ERJ2GEJ 561 X (560 Ω)		
R89	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R90	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		
R91	7030005060	S.RES ERJ2GEJ 333 X (33 kΩ)		
R92	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R93	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R94	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R95	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R96	7030009270	S.RES ERJ2GEJ 821 X (820 Ω)		
R97	7030008280	S.RES ERJ2GEJ 271 X (270 Ω)		
R98	7030007290	S.RES ERJ2GEJ 222 X (2.2 kΩ)		
R100	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R101	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R102	7030005700	S.RES ERJ2GEJ 274 X (270 kΩ)		
R103	7030005160	S.RES ERJ2GEJ 105 X (1 MΩ)		
R104	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R105	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R106	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R107	7030004970	S.RES ERJ2GEJ 470 X (47 Ω)		
R108	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R109	7030008300	S.RES ERJ2GEJ 184 X (180 kΩ)		
R110	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R111	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R113	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R115	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R116	7030007060	S.RES ERJ2GEJ 684X (680 kΩ)		
R117	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R118	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R121	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R122	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R123	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R124	7030005170	S.RES ERJ2GEJ 474 X (470 kΩ)		
R125	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		
R131	7510001730	S.TMR ERTJOEP 473J		
R132	7030010080	S.RES ERJ2RHD 104 X (100 kΩ)		
R133	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R134	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R135	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R136	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R137	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R138	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R140	7030010040	S.RES ERJ2GE-JPW		
R141	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R142	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R143	7030007340	S.RES ERJ2GEJ 153 X (15 kΩ)		
R144	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R145	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R146	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R147	7030007350	S.RES ERJ2GEJ 393 X (39 kΩ)		
R148	7030005080	S.RES ERJ2GEJ 823 X (82 kΩ)		
R149	7030006610	S.RES ERJ2GEJ 394 X (390 kΩ)		
R150	7030008300	S.RES ERJ2GEJ 184 X (180 kΩ)		
R151	7030005030	S.RES ERJ2GEJ 152 X (1.5 kΩ)		
R152	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R153	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R154	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R155	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R156	7030005170	S.RES ERJ2GEJ 474 X (470 kΩ)		
R157	7030007290	S.RES ERJ2GEJ 222 X (2.2 kΩ)		
R159	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R160	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R161	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R162	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R163	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R164	7030008410	S.RES ERJ2GEJ 392 X (3.9 kΩ)		
R165	7030008410	S.RES ERJ2GEJ 392 X (3.9 kΩ)		
R166	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R167	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R168	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R169	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R170	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R171	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R172	7030005700	S.RES ERJ2GEJ 274 X (270 kΩ)		
R173	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R174	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R175	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		
R176	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R177	7030005060	S.RES ERJ2GEJ 333 X (33 kΩ)		
R178	7030007270	S.RES ERJ2GEJ 151 X (150 Ω)		
R179	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R180	7030005160	S.RES ERJ2GEJ 105 X (1 MΩ)		
R181	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R182	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R183	7030006020	S.RES RR0510P-682-D (6.8 kΩ)		
R184	7030008250	S.RES RR0510P-562-D (5.6 kΩ)		
R185	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R186	7030005310	S.RES ERJ2GEJ 124 X (120 kΩ)		
R187	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R190	7030007280	S.RES ERJ2GEJ 331 X (330 Ω)		
R191	7030008300	S.RES ERJ2GEJ 184 X (180 kΩ)		
R192	7030005720	S.RES ERJ2GEJ 563 X (56 kΩ)		
R193	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R194	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R195	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R196	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R197	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R198	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R200	7030005040	S.RES ERJ2GEJ 472 X (4.7 kΩ)		
R201	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R206	7030005110	S.RES ERJ2GEJ 224 X (220 kΩ)		
R207	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R208	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R209	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ)		
R210	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R211	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R212	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R213	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R214	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R215	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R220	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		

US2=F14S for USA, EU2=F14S for EURO, GE2=F14S for GEN S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R221	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R222	7030005060	S.RES ERJ2GEJ 333 X (33 kΩ) (GE1, US2, GE2)		
R222	7030005220	S.RES ERJ2GEJ 223 X (22 kΩ) (EU1, EU2)		
R223	7030005060	S.RES ERJ2GEJ 333 X (33 kΩ)		
R224	7030005000	S.RES ERJ2GEJ 471 X (470 Ω)		
R225	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R226	7210003061	VAR TP76N00N-15F-103-2256		
R227	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R228	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R229	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R230	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R231	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R232	7030007300	S.RES ERJ2GEJ 332 X (3.3 kΩ)		
R233	7030007300	S.RES ERJ2GEJ 332 X (3.3 kΩ)		
R234	7030005100	S.RES ERJ2GEJ 154 X (150 kΩ)		
R235	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R236	7030005230	S.RES ERJ2GEJ 334 X (330 kΩ)		
R237	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R238	7410001140	S.ARY EXB28V104JX		
R240	7030005590	S.RES ERJ2GEJ 680 X (68 Ω)		
R241	7030010040	S.RES ERJ2GE-JPW (US2, EU2, GE2)		
R242	7030010040	S.RES ERJ2GE-JPW (US2, EU2, GE2)		
R251	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R252	7030005530	S.RES ERJ2GEJ 100 X (10 Ω)		
R254	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R255	7030008010	S.RES ERJ2GEJ 123 X (12 kΩ)		
R256	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R257	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R258	7410001140	S.ARY EXB28V104JX		
R259	7030005120	S.RES ERJ2GEJ 102 X (1 kΩ)		
R260	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R261	7410001130	S.ARY EXB28V102JX		
R262	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R263	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R264	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R265	7410001130	S.ARY EXB28V102JX		
R266	7030007340	S.RES ERJ2GEJ 153 X (15 kΩ)		
R271	7030005160	S.RES ERJ2GEJ 105 X (1 MΩ)		
R272	7030005160	S.RES ERJ2GEJ 105 X (1 MΩ)		
R273	7030005160	S.RES ERJ2GEJ 105 X (1 MΩ)		
R274	7030005070	S.RES ERJ2GEJ 683 X (68 kΩ)		
R275	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R276	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R277	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R278	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R280	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R284	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R287	7030009280	S.RES ERJ2GE		
R288	7030008400	S.RES ERJ2GEJ 182 X (1.8 kΩ)		
R291	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R292	7030005090	S.RES ERJ2GEJ 104 X (100 kΩ)		
R293	7030008290	S.RES ERJ2GEJ 183 X (18 kΩ)		
R294	7030005600	S.RES ERJ2GEJ 273 X (27 kΩ)		
R295	7030005240	S.RES ERJ2GEJ 473 X (47 kΩ)		
R301	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R302	7030004980	S.RES ERJ2GEJ 101 X (100 Ω)		
R303	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R304	7030009290	S.RES ERJ2GEJ 562 X (5.6 kΩ)		
R305	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R306	7030005050	S.RES ERJ2GEJ 103 X (10 kΩ)		
R307	7030003350	S.RES ERJ3GEYJ 181 V (180 Ω)		
C2	4030017620	S.CER ECJ0EC1H100C		
C3	4030017410	S.CER ECJ0EC1H240J		
C4	4030017380	S.CER ECJ0EC1H050B		
C5	4030017650	S.CER ECJ0EC1H270J		
C6	4030017460	S.CER ECJ0EB1E102K		
C7	4030017460	S.CER ECJ0EB1E102K		
C8	4030017390	S.CER ECJ0EC1H180J		
C9	4030017640	S.CER ECJ0EC1H150J		
C10	4030017460	S.CER ECJ0EB1E102K		
C11	4030009990	S.CER C1608 CH 1H 200J-T		
C12	4030007090	S.CER C1608 CH 1H 470J-T		
C13	4030010000	S.CER C1608 CH 1H 510J-T		
C14	4030011530	S.CER C1608 CH 1H 110J-T		
C15	4030017460	S.CER ECJ0EB1E102K		
C16	4030017400	S.CER ECJ0EC1H220J		
C17	4030017630	S.CER ECJ0EC1H120J		
C18	4030017380	S.CER ECJ0EC1H050B		
C19	4030017460	S.CER ECJ0EB1E102K		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C20	4030017430	S.CER ECJ0EC1H101J		
C22	4030017340	S.CER ECJ0EC1H010B		
C23	4030016930	S.CER ECJ0EB1A104K		
C24	4030017460	S.CER ECJ0EB1E102K		
C25	4030017580	S.CER ECJ0EC1H060C		
C26	4030017430	S.CER ECJ0EC1H101J		
C27	4030017460	S.CER ECJ0EB1E102K		
C28	4030017370	S.CER ECJ0EC1H3R5B		
C29	4030017560	S.CER ECJ0EC1H2R5B		
C30	4030016930	S.CER ECJ0EB1A104K		
C31	4030016930	S.CER ECJ0EB1A104K		
C32	4030017460	S.CER ECJ0EB1E102K		
C33	4030017420	S.CER ECJ0EC1H470J		
C36	4030017460	S.CER ECJ0EB1E102K		
C37	4030016790	S.CER ECJ0EB1C103K		
C38	4030017360	S.CER ECJ0EC1H030B		
C39	4030017460	S.CER ECJ0EB1E102K		
C40	4030017550	S.CER ECJ0EC1H1R5B		
C41	4030017620	S.CER ECJ0EC1H100C		
C42	4030017460	S.CER ECJ0EB1E102K		
C43	4030017460	S.CER ECJ0EB1E102K		
C44	4030017570	S.CER ECJ0EC1H040B		
C45	4030017460	S.CER ECJ0EB1E102K		
C48	4030016790	S.CER ECJ0EB1C103K		
C49	4030017350	S.CER ECJ0EC1H020B		
C50	4030017460	S.CER ECJ0EB1E102K		
C51	4030017460	S.CER ECJ0EB1E102K		
C52	4030017630	S.CER ECJ0EC1H120J		
C53	4030016790	S.CER ECJ0EB1C103K		
C54	4030017460	S.CER ECJ0EB1E102K		
C55	4030017570	S.CER ECJ0EC1H040B		
C56	4030017400	S.CER ECJ0EC1H220J		
C57	4030017460	S.CER ECJ0EB1E102K		
C58	4030017460	S.CER ECJ0EB1E102K		
C59	4030017460	S.CER ECJ0EB1E102K		
C60	4030016790	S.CER ECJ0EB1C103K		
C61	4030017430	S.CER ECJ0EC1H101J		
C62	4030017680	S.CER ECJ0EC1H820J		
C63	4030017420	S.CER ECJ0EC1H470J		
C64	4030016790	S.CER ECJ0EB1C103K		
C65	4030017460	S.CER ECJ0EB1E102K		
C66	4030017460	S.CER ECJ0EB1E102K		
C67	4030017460	S.CER ECJ0EB1E102K		
C68	4030017430	S.CER ECJ0EC1H101J		
C69	4030017430	S.CER ECJ0EC1H101J		
C70	4030017430	S.CER ECJ0EC1H101J		
C71	4030016930	S.CER ECJ0EB1A104K		
C72	4030017460	S.CER ECJ0EB1E102K		
C73	4030017460	S.CER ECJ0EB1E102K		
C74	4030017460	S.CER ECJ0EB1E102K		
C75	4550006250	S.TAN TEESVA 1A 106M8L		
C76	4030016930	S.CER ECJ0EB1A104K		
C77	4030017460	S.CER ECJ0EB1E102K		
C78	4030017460	S.CER ECJ0EB1E102K		
C79	4030016930	S.CER ECJ0EB1A104K		
C80	4030016930	S.CER ECJ0EB1A104K		
C81	4030017490	S.CER C1608 JB 1A 105K-T		
C82	4030017460	S.CER ECJ0EB1E102K		
C83	4030017460	S.CER ECJ0EB1E102K		
C84	4030017730	S.CER ECJ0EB1E471K		
C85	4030017460	S.CER ECJ0EB1E102K		
C86	4030017430	S.CER ECJ0EC1H101J		
C87	4030017650	S.CER ECJ0EC1H270J		
C88	4030017460	S.CER ECJ0EB1E102K		
C89	4030016790	S.CER ECJ0EB1C103K		
C90	4030017460	S.CER ECJ0EB1E102K		
C91	4510005430	S.ELE ECEV0JA220SR		
C92	4030017430	S.CER ECJ0EC1H101J		
C93	4030017650	S.CER ECJ0EC1H270J		
C94	4030017420	S.CER ECJ0EC1H470J		
C95	4030017460	S.CER ECJ0EB1E102K		
C96	4030017460	S.CER ECJ0EB1E102K		
C97	4030017420	S.CER ECJ0EC1H470J		
C99	4030017460	S.CER ECJ0EB1E102K		
C100	4030017620	S.CER ECJ0EC1H100C		
C101	4030017460	S.CER ECJ0EB1E102K		
C102	4030017590	S.CER ECJ0EC1H070C		
C103	4030017360	S.CER ECJ0EC1H030B		
C104	4030017460	S.CER ECJ0EB1E102K		
C105	4030017460	S.CER ECJ0EB1E102K		
C106	4030017420	S.CER ECJ0EC1H470J		

US1=F14 for USA, EU1=F14 for EURO, GE1=F14 for GEN S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C107	4030017460	S.CER ECJ0EB1E102K		
C108	4030016790	S.CER ECJ0EB1C103K		
C109	4030017460	S.CER ECJ0EB1E102K		
C110	4030017730	S.CER ECJ0EB1E471K		
C111	4030017460	S.CER ECJ0EB1E102K		
C113	4030017540	S.CER ECJ0EC1HR75B		
C114	4030017660	S.CER ECJ0EC1H330J		
C115	4030017660	S.CER ECJ0EC1H330J		
C116	4030017460	S.CER ECJ0EB1E102K		
C118	4030017530	S.CER ECJ0EC1H0R5B		
C119	4030017460	S.CER ECJ0EB1E102K		
C120	4030017430	S.CER ECJ0EC1H101J		
C121	4030017390	S.CER ECJ0EC1H180J		
C122	4030017660	S.CER ECJ0EC1H330J		
C123	4030017510	S.CER ECJ0EC1H680J		
C124	4030017440	S.CER ECJ0EC1H221J		
C126	4030017660	S.CER ECJ0EC1H330J		
C127	4030017420	S.CER ECJ0EC1H470J		
C129	4030017340	S.CER ECJ0EC1H010B		
C132	4030016930	S.CER ECJ0EB1A104K		
C133	4030017400	S.CER ECJ0EC1H220J		
C134	4030017390	S.CER ECJ0EC1H180J		
C135	4030017460	S.CER ECJ0EB1E102K		
C137	4030016790	S.CER ECJ0EB1C103K		
C138	4030017420	S.CER ECJ0EC1H470J		
C139	4030016930	S.CER ECJ0EB1A104K		
C140	4030016930	S.CER ECJ0EB1A104K		
C141	4030017460	S.CER ECJ0EB1E102K		
C143	4030017460	S.CER ECJ0EB1E102K		
C144	4030017460	S.CER ECJ0EB1E102K		
C145	4030017460	S.CER ECJ0EB1E102K		
C146	4550000270	S.TAN TEESVA 1E 474M8L		
C147	4550004040	S.TAN TEESVA 0J 685M-8L		
C148	4550006250	S.TAN TEESVA 1A 106M8L		
C149	4550000270	S.TAN TEESVA 1E 474M8L		
C150	4030018860	S.CER ECJ0EB0J105K		
C151	4030016930	S.CER ECJ0EB1A104K		
C152	4030017420	S.CER ECJ0EC1H470J		
C153	4030017420	S.CER ECJ0EC1H470J		
C154	4030017420	S.CER ECJ0EC1H470J		
C155	4030017460	S.CER ECJ0EB1E102K		
C156	4030017460	S.CER ECJ0EB1E102K		
C157	4030017620	S.CER ECJ0EC1H100C		
C158	4030016930	S.CER ECJ0EB1A104K		
C159	4030017460	S.CER ECJ0EB1E102K		
C160	4030016930	S.CER ECJ0EB1A104K		
C161	4030017620	S.CER ECJ0EC1H100C		
C162	4030017500	S.CER ECJ0EC1H560J		
C163	4030017570	S.CER ECJ0EC1H040B		
C164	4030017590	S.CER ECJ0EC1H070C		
C165	4030016790	S.CER ECJ0EB1C103K		
C166	4030017360	S.CER ECJ0EC1H030B		
C167	4030016930	S.CER ECJ0EB1A104K		
C168	4030016930	S.CER ECJ0EB1A104K		
C169	4030016930	S.CER ECJ0EB1A104K		
C171	4030017460	S.CER ECJ0EB1E102K		
C172	4030017460	S.CER ECJ0EB1E102K		
C174	4030017530	S.CER ECJ0EC1H0R5B		
C175	4030017550	S.CER ECJ0EC1H1R5B		
C176	4030017640	S.CER ECJ0EC1H150J		
C182	4030017460	S.CER ECJ0EB1E102K		
C183	4030017620	S.CER ECJ0EC1H100C		
C184	4030017460	S.CER ECJ0EB1E102K		
C185	4030016930	S.CER ECJ0EB1A104K		
C186	4030016930	S.CER ECJ0EB1A104K		
C188	4030017460	S.CER ECJ0EB1E102K		
C190	4030017380	S.CER ECJ0EC1H050B		
C191	4030017570	S.CER ECJ0EC1H040B		
C192	4030017360	S.CER ECJ0EC1H030B		
C193	4030017360	S.CER ECJ0EC1H030B		
C194	4030017380	S.CER ECJ0EC1H050B		
C195	4030017570	S.CER ECJ0EC1H040B		
C200	4030017420	S.CER ECJ0EC1H470J		
C201	4030018860	S.CER ECJ0EB0J105K		
C202	4030016930	S.CER ECJ0EB1A104K		
C203	4030017460	S.CER ECJ0EB1E102K		
C205	4030017400	S.CER ECJ0EC1H220J		
C205	4030017620	S.CER ECJ0EC1H100C		
C206	4030017630	S.CER ECJ0EC1H120J		
C207	4030017620	S.CER ECJ0EC1H100C		
C208	4030017630	S.CER ECJ0EC1H120J		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C209	4030017460	S.CER ECJ0EB1E102K		
C211	4030018860	S.CER ECJ0EB0J105K		
C213	4030016930	S.CER ECJ0EB1A104K		
C221	4030016930	S.CER ECJ0EB1A104K		
C222	4030016930	S.CER ECJ0EB1A104K		
C223	4030016930	S.CER ECJ0EB1A104K		
C224	4030016930	S.CER ECJ0EB1A104K		
C225	4030017460	S.CER ECJ0EB1E102K		
C226	4550005980	S.TAN TEESVA 1A 475M8L		
C227	4030016790	S.CER ECJ0EB1C103K		
C228	4510004630	S.ELE ECEV1CA100SR		
C229	4030017460	S.CER ECJ0EB1E102K		
C230	4030016930	S.CER ECJ0EB1A104K		
C231	4030016790	S.CER ECJ0EB1C103K		
C232	4030016930	S.CER ECJ0EB1A104K		
C233	4030016790	S.CER ECJ0EB1C103K		
C234	4030017460	S.CER ECJ0EB1E102K		
C235	4030016790	S.CER ECJ0EB1C103K		
C236	4030017460	S.CER ECJ0EB1E102K		
C237	4510005430	S.ELE ECEV0JA220SR		
C238	4030017460	S.CER ECJ0EB1E102K		
C241	4030016930	S.CER ECJ0EB1A104K		
C242	4030016930	S.CER ECJ0EB1A104K		
C243	4030016790	S.CER ECJ0EB1C103K		
C244	4030017460	S.CER ECJ0EB1E102K		
C251	4030016970	S.CER ECJ0EB1C223K		
C252	4030017740	S.CER ECJ0EB1E821K		
C253	4030017740	S.CER ECJ0EB1E821K		
C254	4030016930	S.CER ECJ0EB1A104K		
C255	4030016950	S.CER ECJ0EB1A473K		
C256	4030016940	S.CER ECJ0EB1A393K		
C257	4030016930	S.CER ECJ0EB1A104K		
C258	4030017790	S.CER ECJ0EB1E682K		
C259	4030018860	S.CER ECJ0EB0J105K		
C260	4030017730	S.CER ECJ0EB1E471K		
C261	4030016930	S.CER ECJ0EB1A104K		
C264	4510004630	S.ELE ECEV1CA100SR		
C265	4030017460	S.CER ECJ0EB1E102K		
C266	4030016930	S.CER ECJ0EB1A104K		
C269	4030017720	S.CER ECJ0EB1H331K		
C270	4030016950	S.CER ECJ0EB1A473K		
C271	4030016950	S.CER ECJ0EB1A473K		
C272	4030016950	S.CER ECJ0EB1A473K		
C273	4030016950	S.CER ECJ0EB1A473K		
C274	4030016950	S.CER ECJ0EB1A473K		
C275	4030016970	S.CER ECJ0EB1C223K		
C276	4030016950	S.CER ECJ0EB1A473K		
C277	4030016930	S.CER ECJ0EB1A104K		
C278	4030017430	S.CER ECJ0EC1H101J		
C279	4030018560	S.CER C2012 JB 1A 475K-T		
C280	4030018110	S.CER ECJ0EB1H272K		
C281	4030017780	S.CER ECJ0EB1E472K		
C282	4030017710	S.CER ECJ0EC1H181J		
C283	4030018900	S.CER ECJ0EB0J474K		
C284	4030016930	S.CER ECJ0EB1A104K		
C285	4030016930	S.CER ECJ0EB1A104K		
C286	4030017460	S.CER ECJ0EB1E102K		
C287	4550006250	S.TAN TEESVA 1A 106M8L		
C288	4030017460	S.CER ECJ0EB1E102K		
C289	4030016930	S.CER ECJ0EB1A104K		
C290	4030017920	S.CER ECJ0EB1A683K		
C291	4030016780	S.CER ECJ0EB1C153K		
C292	4030016930	S.CER ECJ0EB1A104K		
C293	4030017740	S.CER ECJ0EB1E821K		
C294	4030016930	S.CER ECJ0EB1A104K		
C295	4030018110	S.CER ECJ0EB1H272K		
C296	4030018240	S.CER ECJ0EB1E562K		
C297	4030017710	S.CER ECJ0EC1H181J		
C298	4030018090	S.CER ECJ0EB1C822K		
C299	4030017510	S.CER ECJ0EC1H680J		
C300	4030017450	S.CER ECJ0EB1E271K		
C306	4030017460	S.CER ECJ0EB1E102K		
C307	4030017460	S.CER ECJ0EB1E102K		
C308	4030017460	S.CER ECJ0EB1E102K		
C309	4030017460	S.CER ECJ0EB1E102K		
C310	4030016930	S.CER ECJ0EB1A104K		
C311	4030017460	S.CER ECJ0EB1E102K		
C312	4030017420	S.CER ECJ0EC1H470J		
C313	4030017420	S.CER ECJ0EC1H470J		
C314	4030017460	S.CER ECJ0EB1E102K		
C315	4030017460	S.CER ECJ0EB1E102K		

US2=F14S for USA, EU2=F14S for EURO, GE2=F14S for GEN S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C316	4030016930	S.CER ECJ0EB1A104K		
C317	4550007080	S.TAN TEESVA 1C 106M8R		
C318	4030016930	S.CER ECJ0EB1A104K		
C319	4030016930	S.CER ECJ0EB1A104K		
C320	4030017730	S.CER ECJ0EB1E471K		
C321	4030017460	S.CER ECJ0EB1E102K		
C322	4030016950	S.CER ECJ0EB1A473K		
C323	4030016950	S.CER ECJ0EB1A473K		
C324	4030017420	S.CER ECJ0EC1H470J		
C325	4550006250	S.TAN TEESVA 1A 106M8L		
C326	4510006940	S.ELE EEVFC0J101P		
C335	4030018860	S.CER ECJ0EB0J105K		
C339	4030016930	S.CER ECJ0EB1A104K		
C340	4030016930	S.CER ECJ0EB1A104K		
C341	4030016930	S.CER ECJ0EB1A104K		
C342	4030017630	S.CER ECJ0EC1H120J		
C343	4030017580	S.CER ECJ0EC1H060C		
C344	4030017640	S.CER ECJ0EC1H150J		
C345	4030016930	S.CER ECJ0EB1A104K		
C346	4030016930	S.CER ECJ0EB1A104K		
C347	4030016790	S.CER ECJ0EB1C103K		
C348	4030016930	S.CER ECJ0EB1A104K		
C349	4030016930	S.CER ECJ0EB1A104K		
C350	4030017460	S.CER ECJ0EB1E102K		
J1	6510021900	S.CNR BM02B-ASRS-TF		
J2	6450001680	CNR HSJ1122-010010		
J3	6450002250	CNR HSJ1456-010320		
J4	6510018430	S.CNR AXN330C038P		
J5	6510021900	S.CNR BM02B-ASRS-TF (US2, EU2, GE2)		
F1	5210000830	S.FUS ERBFE3R00U		
DS1	5040002670	S.LED CL-165HR/YG		
MC1	7700002540	MIC SKP-4538		
S1	2260002840	SW SKHLLFA010		
S2	2260002800	S.SW SW-167 (SKQTLAE010)		
S3	2260002800	S.SW SW-167 (SKQTLAE010)		
S4	2250000490	ECR TP70TF5163-15.9F-2775 (US1, EU1, GE1)		
EP1	0910057892	PCB		
EP2	6910015600	S.BEA ACZ1005Y-241 (240 Ω)		
EP3	6910015370	S.BEA ACZ1005Y-102-T		
EP4	6910015370	S.BEA ACZ1005Y-102-T		
EP6	6910015370	S.BEA ACZ1005Y-102-T		

[CONNECTOR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C501	4030017460	S.CER ECJ0EB1E102K		
C502	4030016930	S.CER ECJ0EB1A104K		
J501	6910016390	CNR IMSA-9230B-1-02Z145-PT1		
EP501	0910057910	PCB B-6160		

[ANTENNA UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
ANT EP601	0910057901	PCB B-6159B		

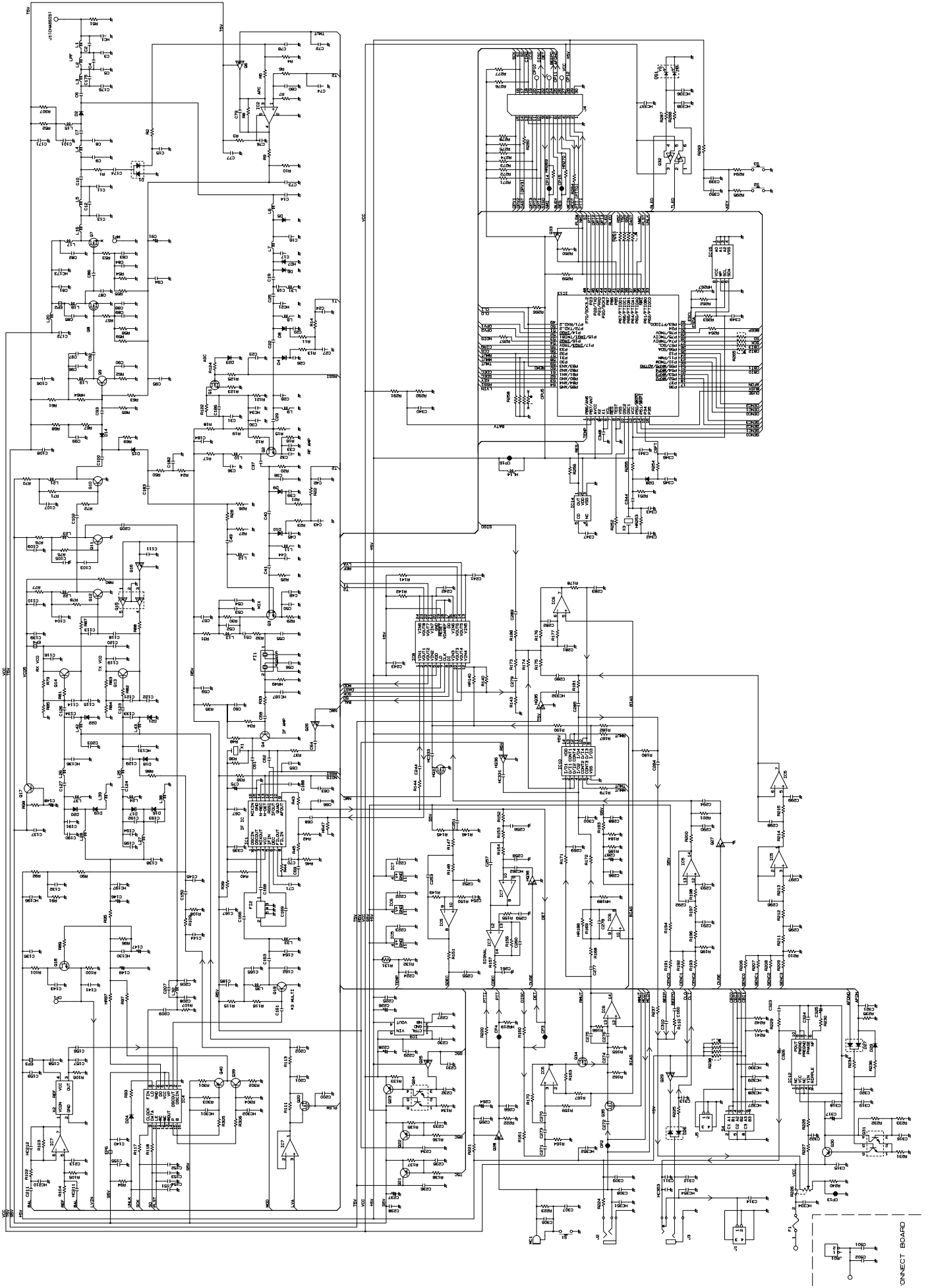
[CHASSIS UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
J1	6910015910	CNR ANT CONNECTOR-104		
J2	6910015860	CNR IMSA-6277S-02A-G		
S1	2260002870	SW AS-243-A13 (US2, EU2, GE2)		
SP1	2510001060	SP K036NA500-47		
W1	8900009640	CBL OPC-963		
W2	8900009640	CBL OPC-963 (US2, EU2, GE2)		

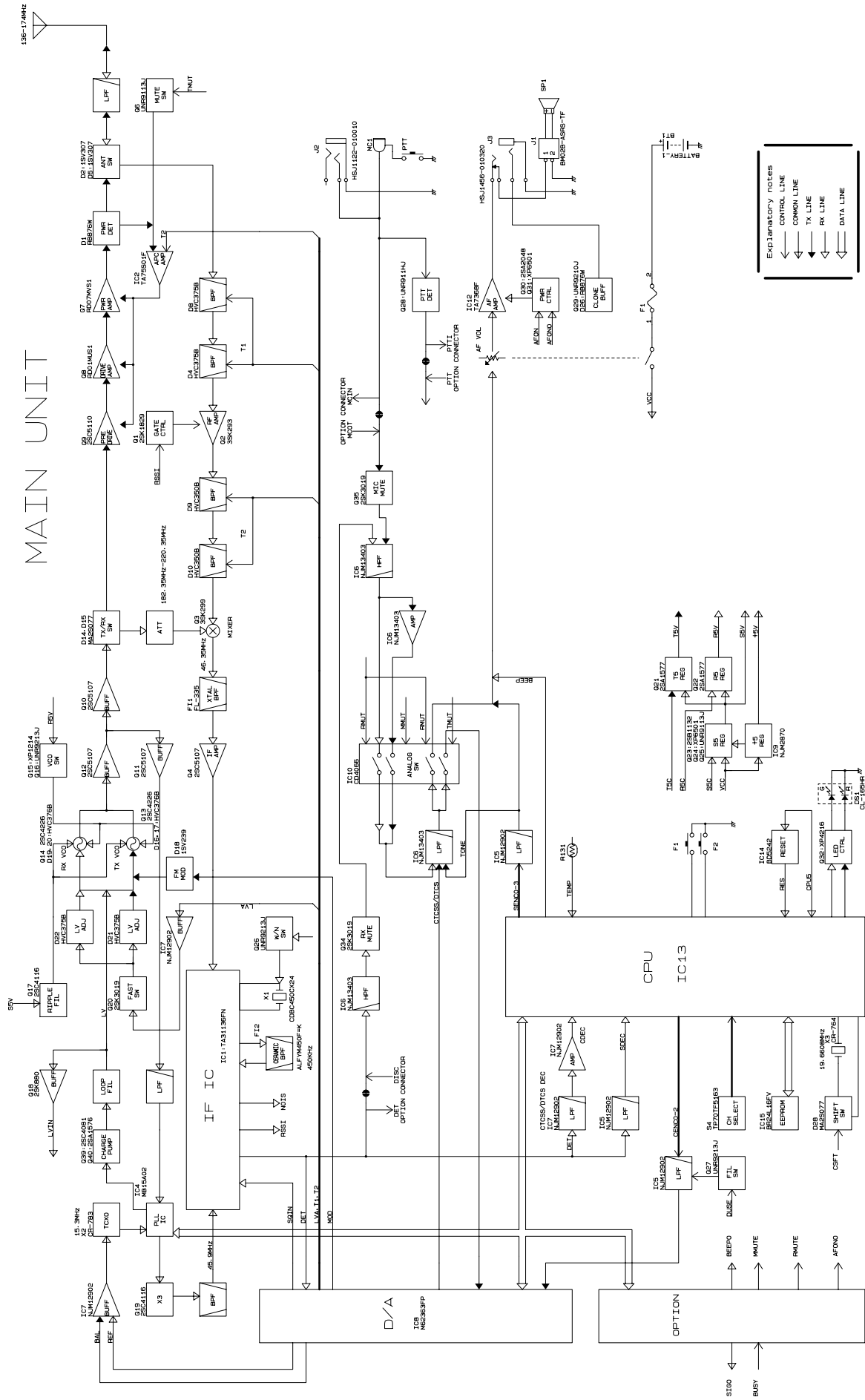
US1=F14 for USA, EU1=F14 for EURO, GE1=F14 for GEN S.=Surface mount

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

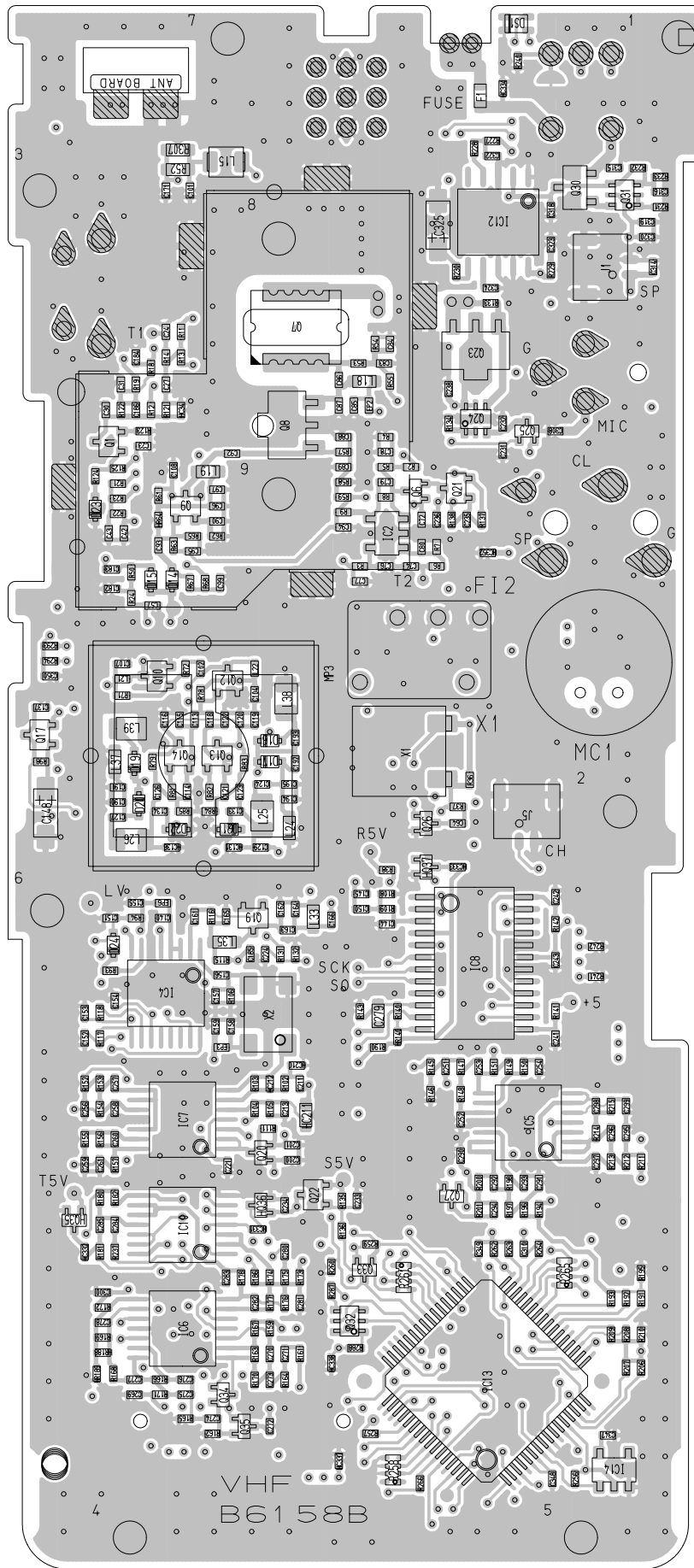
MAIN UNIT CIRCUIT DIAGRAM



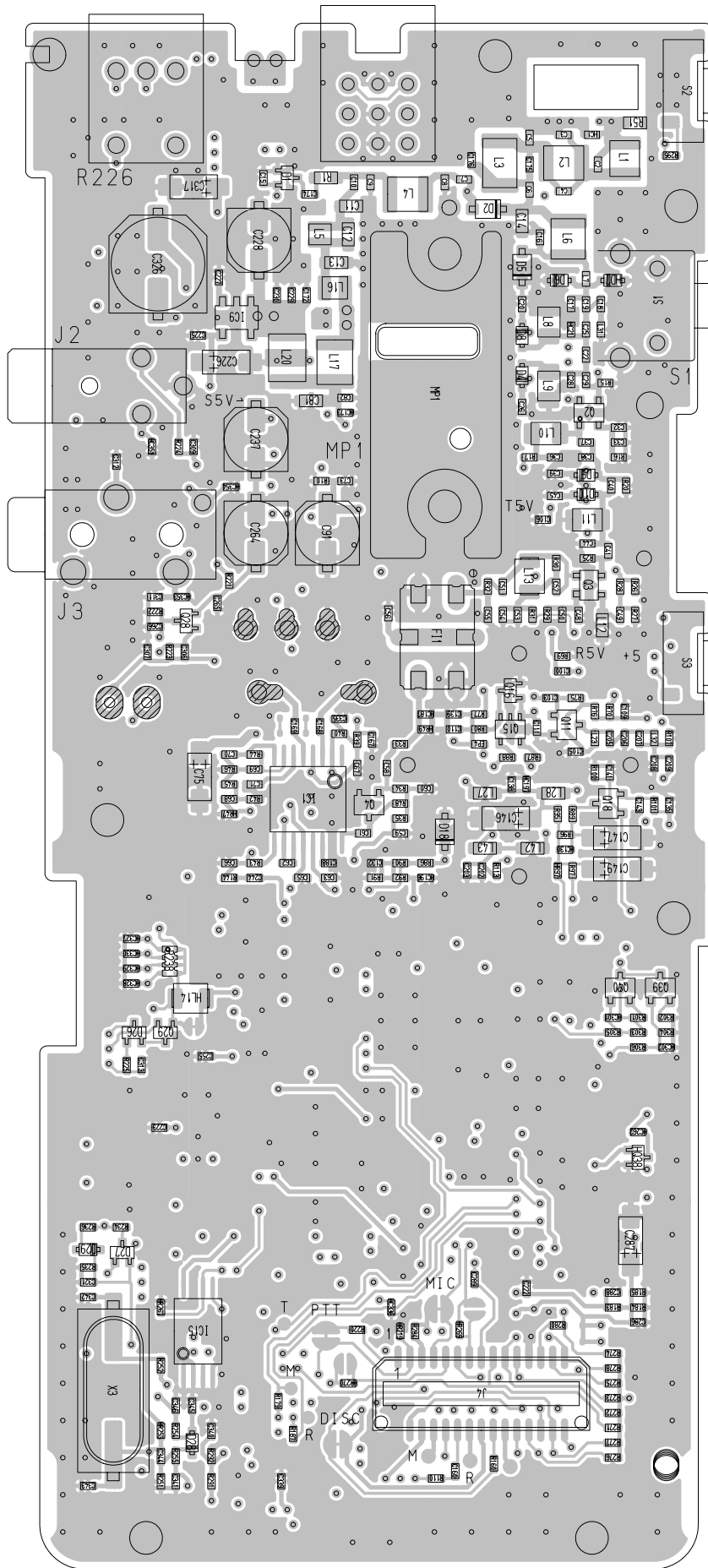
MAIN UNIT BLOCK DIAGRAM



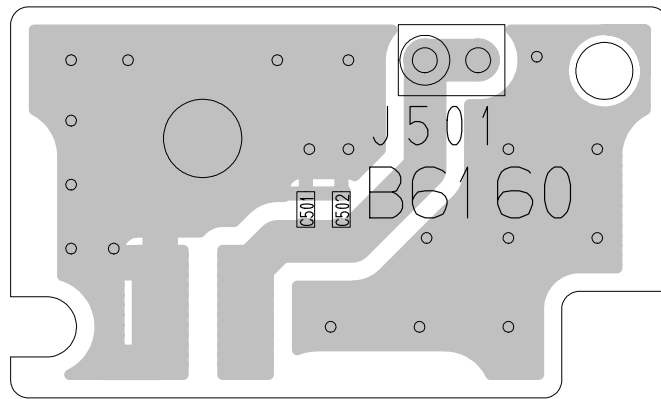
MAIN UNIT BOARD LAYOUT (TOP)



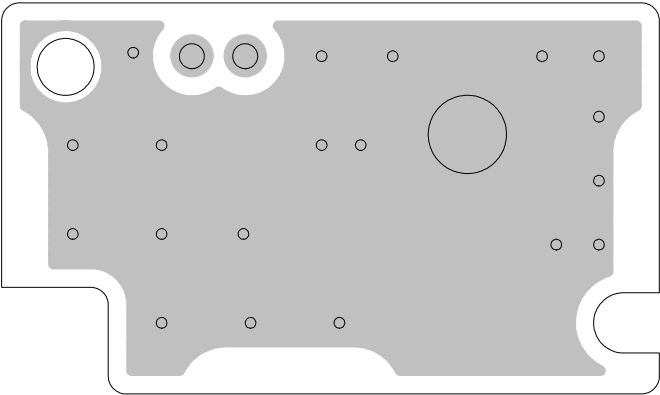
MAIN UNIT BOARD LAYOUT (BOTTOM)



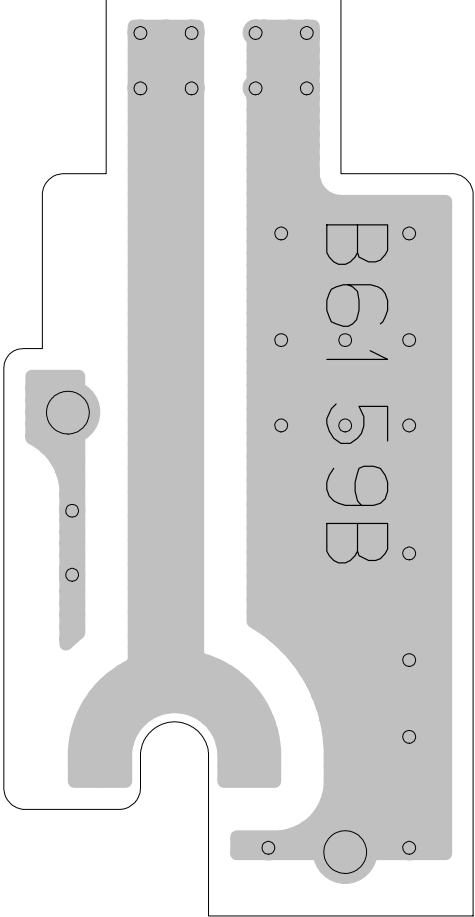
CONNECTOR UNIT BOARD LAYOUT (TOP)



CONNECTOR UNIT BOARD LAYOUT (BOTTOM)



ANTENNA UNIT BOARD LAYOUT (TOP)



ANTENNA UNIT BOARD LAYOUT (BOTTOM)

