

NJS4310D is designed for the front end of radar system. It features a small size and a light weight operable at any frequency between 3.02GHz and 3.08GHz

This front end module consists of GaAs FET low noise amplifier. image rejection mixer. local VCO with buffer amplifier.

FET monitoring circuit is included to monitor FET drain current. The stability of the local VCO frequency by the input RF power is increased effectively by the buffer amplifier which is located between image rejection mixer and local VCO.

--- ELECTRICAL --- < at 25 °C >

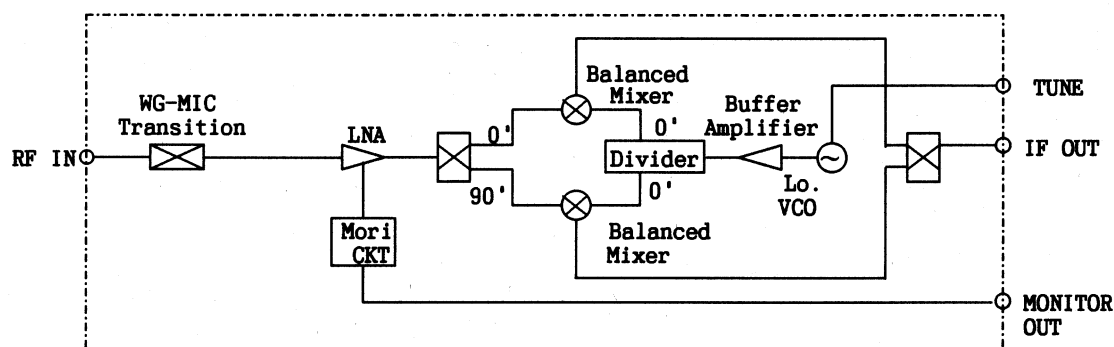
	Min	Typical	Max	Unit
Operating Voltage .....	11.8	12.0	12.2	V
Operating current .....	-	-	100	mA
Local frequency $V_T = 4V$ .....	-	-	3.05	GHz
$V_T = 24V$ .....	3.17	-	-	GHz
Noise figure(Note3) .....	-	-	3.0	dB
Conversion gain(Note3) .....	6.0	-	-	dB
1dB Gain compression point(IF Port) ..	-5	-	-	dBm
Monitor voltage .....	-	50	-	mV
RF single pulse burnout(Note1) .....	-	-	600	mW
RF repetitive Pulse burnout(Note2) ...	-	-	400	mW

Note1,  $f=3.05\text{GHz}$   $P_d=10\text{nsec}$

Note2,  $f=3.05\text{GHz}$   $P_d=1\mu\text{s}$  Duty=0.001

Note3, Temperature Range  $T_a=-30\text{deg.} - +90\text{deg.}$

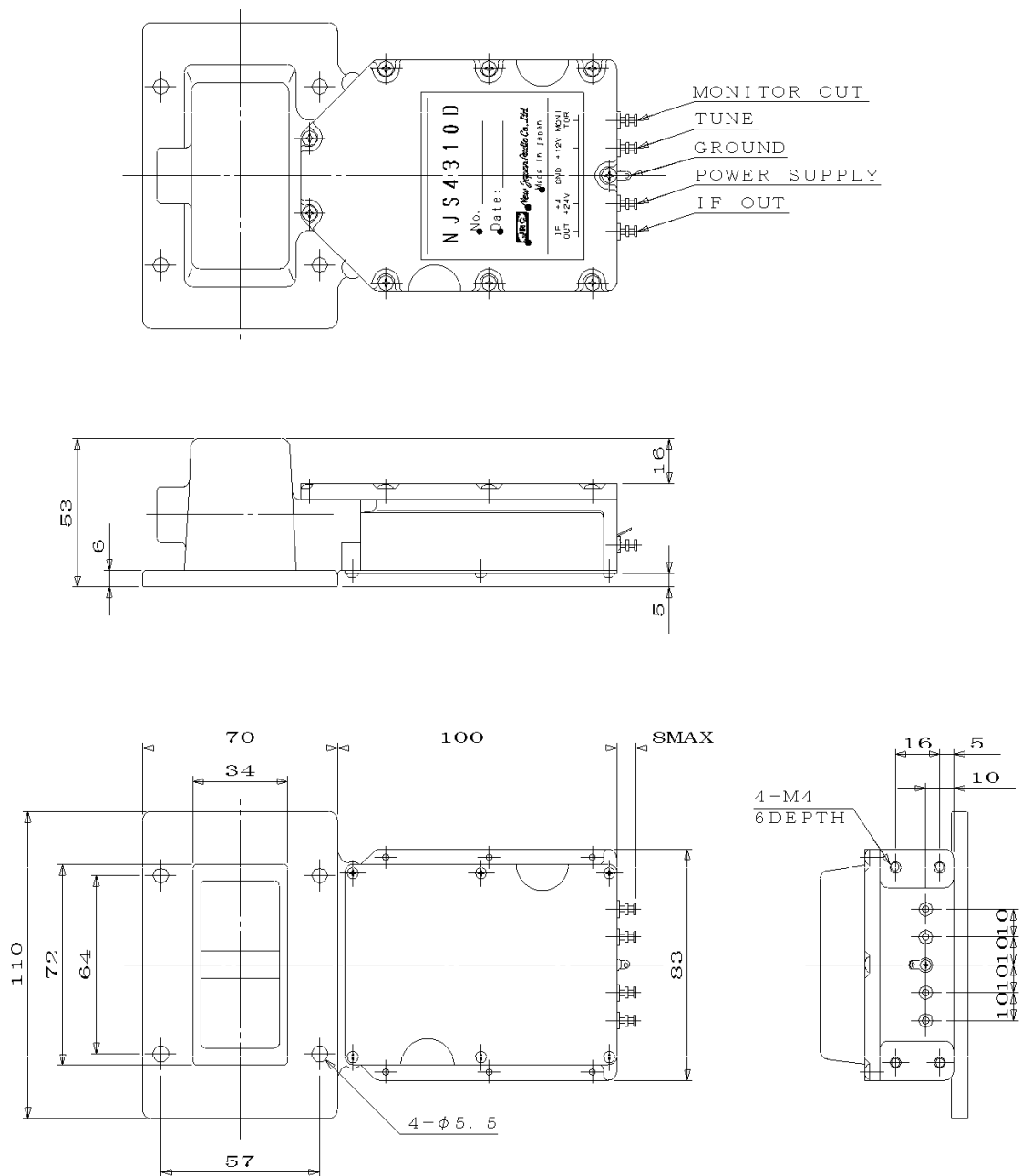
--- BLOCK DIAGRAM ---



For further information on the use of the front end, please contact New JRC. New JRC reserves the right to change the specification of goods without notice.



## NJS4310D



Unit : mm